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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/943,880	08/30/2001	Michael B. Ball	2769.6US (95-1118.5)	2391
24247	7590 04/23/2003			
TRASK BRITT P.O. BOX 2550			EXAMINER	
SALT LAKE CITY, UT 84110			CHAMBLISS, ALONZO	
			ART UNIT	PAPER NUMBER
			2827	
			DATE MAILED: 04/23/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

		A	pplication No.	Applicant(s)
. Office Action Summary			9/943,880	BALL, MICHAEL B.
		E	xaminer	Art Unit
		A	lonzo Chambliss	2827
Period fo	The MAILING DATE of this comn or Reply	nunication appear	s on the cover she	et with the correspondence address
- Exter after - If the - If NO - Failu - Any r	ORTENED STATUTORY PERIOD MAILING DATE OF THIS COMMUNICATION OF THIS COMMUNICATION OF THIS COMMUNICATION OF THIS FROM THE MAILING BETT OF THE MET OF THE ME	ions of 37 CFR 1.136(a) communication. ty (30) days, a reply with m statutory period will ap eply will, by statute, caus	. In no event, however, m in the statutory minimum oply and will expire SIX (6)	ay a reply be timely filed  of thirty (30) days will be considered timely.  MONTHS from the mailing date of this communication
1)[	Responsive to communication(s	) filed on 12 Janu	2000	
2a)⊠	This action is <b>FINAL</b> .			
3)		inis ad	ction is non-final.	
,	closed in accordance with the proper of Claims	actice under <i>Ex p</i>	except for formal parte Quayle, 1935	matters, prosecution as to the merits is C.D. 11, 453 O.G. 213.
4)⊠	Claim(s) <u>1-11</u> is/are pending in th	e application.		
	a) Of the above claim(s) is		om consideration	
5) 🗌 (	Claim(s) is/are allowed.			
6)⊠ (	Claim(s) <u>1-11</u> is/are rejected.			
7) 🗌 (	Claim(s) is/are objected to.			
8) [ (	Claim(s) are subject to rest	riction and/or ele	ction requirement	
	n Papers			
9)∐ Ti	ne specification is objected to by t	he Examiner.		
10)∐ TI	ne drawing(s) filed on is/are	e: a)⊡ accepted c	r b) objected to b	v the Examiner
	Applicant may not request that any o	bjection to the drav	ving(s) be held in ab	evance See 37 CED 1 05(a)
11/23/11	ie proposed drawing correction file	ed on <u>13 Januar</u> y	<u>∕ 2003</u> is: a)⊠ app	proved b) disapproved by the Examina
	ii approved, corrected drawings are r	equired in reply to	this Office action	, and Examine
12) Ir	e oath or declaration is objected t	to by the Examin	er.	
	der 35 U.S.C. §§ 119 and 120			
13)∐ A	cknowledgment is made of a clair	n for foreign prior	ity under 35 U.S.C	C. § 119(a)-(d) or (f).
a)[_]	All b) Some * c) None of:			- ( ) ( ) ( ) (
1.	Certified copies of the priority	documents have	e been received.	
2.	Certified copies of the priority	documents have	e been received in	Application No
	Copies of the certified copies  application from the Interior	of the priority do	cuments have bee	n received in this National Stage
14)	the attached detailed Office action	on domest of the	certified copies no	ot received.
, ; зл. a) Г	The translation of the foreign to	or domestic prior	ity under 35 U.S.C	C. § 119(e) (to a provisional application
15) Ack	The translation of the foreign lange in the translation of the foreign lange in the foreign is made of a claim to the foreign lange.	iguage provision	al application has	been received.
tachment(s)	o . a sidiff	wornesde prior	ny under 35 U.S.(	. 99 120 and/or 121.
☐ Notice of ☐ Information	References Cited (PTO-892) Draftsperson's Patent Drawing Review (F on Disclosure Statement(s) (PTO-1449) P	PTO-948) aper No(s)	4) Interview 5) Notice of 6) Other:	v Summary (PTO-413) Paper No(s) f Informal Patent Application (PTO-152)
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#### **DETAILED ACTION**

1. Amendment B filed on 1/13/03 has been fully considered and made of record in Paper No. 5.

## Response to Arguments

2. Applicant's arguments with respect to claim1-11 have been considered but are moot in view of the new ground(s) of rejection.

## Claim Rejections - 35 USC § 112

- 3. The following is a quotation of the second paragraph of 35 U.S.C. 112:
  The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 4. Claims 1-11 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
- 5. In claim 1, the phrase "said first semiconductor substrate element and the second semiconductor substrate element for locating a portion of the integrated circuitry on the said first semiconductor substrate element adjacent a portion of the integrated circuitry on the second semiconductor substrate element" is vague and indefinite, since it is not clear from the claim how the first and second substrate element are **for locating** a portion of the integrated circuitry on the first and second substrate element.

  Furthermore, it is not clear from the claim how a portion of the circuitry of the first semiconductor element is **adjacent a portion** of the circuitry of the second

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semiconductor element. . Furthermore, it is established by claim 1 that the first and second semiconductor substrate elements are stacked but does not have to be aligned. However, all of the figures show that the dies have to be aligned before severing.

- 6. Claim 1 recites the limitation "said at least one second semiconductor substrate element" in lines 8, 17, and 19. There is insufficient antecedent basis for this limitation in the claim.
- 7. Claim 5 recites the limitation "said vertical alignment" in line 3. There is insufficient antecedent basis for this limitation in the claim.
- 8. Claim 3 recites the limitation "said at least one second semiconductor substrate element" in line 3. There is insufficient antecedent basis for this limitation in the claim.
- 9. Claim 5 recites the limitation "said at least one second semiconductor substrate element" in lines 2 and 4. There is insufficient antecedent basis for this limitation in the claim.
- 10. Claim 5, the phrase "said vertical alignment is effected by aligning said flat of said first semiconductor substrate element and said flat of the at least one second semiconductor substrate element" is vague and indefinite since it is not clear from the claim 1 where the alignment process takes place. Furthermore, it is established by claim 1 that the first and second semiconductor substrate elements are stacked but does not have to be aligned. However, all of the figures show that the dies have to be aligned before severing.
- 11. Claim 8 recites the limitation "conductors" in line 2. There is insufficient antecedent basis for this limitation in the claim.

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12. Claim 1-11 are rejected under 35 U.S.C. 112, second paragraph, as being incomplete for omitting essential steps, such omission amounting to a gap between the steps. See MPEP § 2172.01. The omitted steps are: aligning vertically said first semiconductor substrate element and the at least one second semiconductor substrate element in claim 1.

The prior art made of record and not relied upon is cited primarily to show the process of the instant invention.

#### Conclusion

13. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning the communication or earlier communications from the examiner should be directed to Alonzo Chambliss whose telephone number is (703) 306-9143. The fax phone number for this Group is (703) 308-7722 or 7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-7956.

DAVID L. TALBOTT SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800

**AC**/April 12, 2003